Xilinx Student Edition Software version 4.2i Installation Notes

To ensure the optimum use and operation of your new design tools, install Student Edition v4.2i on the recommended hardware with sufficient memory (RAM and hard disk "swap" space). If you experience problems with the installation, operation, or verification of your installation, see http://xup.msu.edu for FAQs and links to more support.

Supported Operating Systems

The Student Edition v4.2i software supports the following operating systems and versions.

Platform and System Requirements			
	IBM PC or Compatible		
Windows: 98 S	E , NT 4.0 (With Service Pack 4-6a), ME, 2000		
The memory re	quirements for both RAM and hard disk space will vary depending on your target device		
family and size	as well as the unique characteristics of your design		

Equipment and Permissions

The following table provides information about related equipment, permissions, and network connections.

Item	Requirement
Directory Permissions	Write permissions must exist for all directories containing design files to be edited.
	Note To view and/or edit the example projects included in the
	\$XILINX\ISEexamples directory, the directory must be writable. Move the
	projects to a writable directory before working on them in the ISE Project
	Navigator. Open the project in ISE. To move the project to a writable
	location, select Project → Save As .
Hardware Component	Xilinx recommends that you have an IBM-compatible Pentium class machine.
Monitor —	Color VGA operating in the following modes.
	Minimum Resolution: 640 x 480
	Minimum Recommended: 1024 x 768
Mouse	You should have a 2-button (Microsoft Windows compatible) or 3-button
	(Microsoft compatible) mouse.
CD Drive	You should have an ISO9660 compliant drive on your system.
Ports	You should have two ports (one for a mouse and one parallel port for the
	parallel download cable, if needed).
Network Compatibility	The Xilinx Installation program supports TCP/IP networks. If you are using a
	Windows NT operating system, then the TCP/IP protocol needs to be installed
	first. For more information, see the solution record at the following location:
	http://www.support.xilinx.com/techdocs/2510.htm

System Memory Requirements

RAM - 128 MB to 256 MB (dependent on device)

Virtual Memory - 128 MB to 256 MB (dependent on device)

Note: Due to the size and complexity of the Virtex and Virtex-II devices, Xilinx recommends that these designs be compiled using a high-performance computer. 128 MB of RAM as well as 128 MB of virtual memory is required to compile XC9500 designs. For Spartan II and Virtex devices up to 300K gates, Xilinx recommends 256 MB of RAM.

Virtual memory size requirements also vary with the design and constraint set size. By default, Windows 95/98 manages its swap filesize automatically, but for Windows NT, you may need to increase it. Typically, your Windows NT swap file size should be twice as large as your system RAM amount.

It is important to note that slower systems or systems with less than the recommended RAM and/or swap space may exhibit longer runtimes.

Required disk space, 2 GB recommended

Running Setup

- 1. To start the installation, insert the CD #2 into the CD-ROM drive.
- 2. Run the installation program. The installer should automatically start when the CD is inserted. If it does not, from Windows select **Start_Run**. Type **D:\setup.exe** in the "Open" field of the Run window and click "OK". (If your CD-ROM drive is not the "D" drive, substitute the appropriate drive designation.)
- 3. The Welcome window will open, prompting you to register either via the Website, E-Mail, or FAX. **Registration is NOT required for this product**. Click "Next" to continue.
- 4. Accept the software license agreement by clicking the white box next to "I accept the terms of this software license." so that a check mark appears. Click "Next" to continue.
- 5. In the Registration window, enter your Registration ID: **9990-6970-9169**. Click "Next" to continue.
- 6. Either choose the default directory (recommended) or select another directory for the installation. Click "Next" to continue. (Note that if another version of Xilinx software is installed in the indicated directory, you will be prompted to uninstall the previous version.)
- 7. Perform a Typical installation. Click "Next" to continue.
- 8. Click on the white box to place a check mark next to each device family that you wish to install. Note that each device family requires a certain amount of memory on your system. Click "Next" to continue. If you have selected to install the Virtex-2 Pro device, you need to accept the Virtex-2 Pro License Agreement. Click the white box next to "I accept the terms of this software license" so that a check mark appears. Click "OK".
- 9. When the Update Environment window opens, leave the default settings and click "Next" to continue.
- 10. On the following screen, click on "Install" to install the software.
- 11. After the Design Environment is installed, you will be prompted to insert the Xilinx Docs CD. If you wish to install the documentation, insert CD #1 and click "OK", otherwise click "Cancel". When complete, remove the CD #1.
- 12. If you have installed the Documentation, you will be prompted to insert the Xilinx ISE Design Environment Tools CD into the CD drive when installation of the Documentation is complete. Insert CD #1 back into the CD drive and click "OK".
- 13. The MultiLINX cable installer will start. If you use this cable for development, click "Yes" to install the drivers, otherwise click "No".

You may need to reboot your PC to allow the new/modified environment variables and path statement to take effect before you can run the design implementation tools. The Install program will inform you if you need to reboot.

Service Packs

Download the latest service packs for XSE 4.2i at http://university.xilinx.com/univ/xse42.html. The downloads will include a service pack for the XSE tools and an IP update for Core Generator. To install the service pack for the XSE tools, double-click on the executable file and run the installer to completion. To install the IP Update, extract the contents of the ZIP file using **Winzip**

7.0 SR-1 or later and extract the contents of the file to the ISE installation directory. For more information about installing the service packs, as well as the installation process, visit the online tutorial at http://xup.msu.edu/license/v42i/index.htm.

Modelsim

Modelsim is **not included** with the Student Edition. Modelsim can be downloaded at http://www.xilinx.com/ise/mxe2. For information about installing and licensing Modelsim, visit the online tutorial at http://xup.msu.edu/license/v42i/index.htm.

Licensing

******IMPORTANT*******

If you register and receive a license file via email, **DO NOT INSTALL IT**. Student Edition does not require a license file to run the software.

Technical Support

There are **no updates** and **no live technical support** for this product. For technical support, abide by the following process: (1) First contact your Professor, and then (2) refer to the Xilinx University Resource Center at http://xup.msu.edu.

IMPORTANT! Do Not Discard!

ISE Student Edition Version 4.2i

Congratulations on purchasing your Xilinx ISE Student Edition package, version 4.2i, Product ID

IIB960699999

Your Registration ID Number is:

9990-6970-9169

FOR EDUCATIONAL PURPOSES ONLY

The ISE Student Edition Software is intended for student use (Home or Dorm), one copy per student, it is not for use in the school lab. The standard ISE software should be used in the school lab. University staff members may obtain an ISE standard package through the Xilinx University Program by visiting http://www.university.xilinx.com

TECHNICAL SUPPORT

No live technical support (telephone or e-mail) is offered for the Student Edition package. For technical support, students should refer to the Xilinx University Resource Center website at xup.msu.edu.

IMPORTANT! Do Not Discard! Available Resources

DEVELOPMENT BOARDS

Xilinx distributors and third party partners offer an array of state-of-the-art boards which help you design and interface to specific system and application level functions.

LOW COST BOARDS

• XESS Corp,	www.xess.com
Digilent Inc,	www.digilentinc.com
Burch Electronic Designs,	www.burched.com.au
SPECIALTY BOA	RDS
• Celoxica,	www.celoxica.com
Nallatech,	www.nallatech.com
Virtual Computer Corp,	www.vcc.com

^{*} This is a partial list of available board resources

TECHNICAL VENUES (RESOURCES, FORUMS & USER GROUPS)

XUP Resource Center	www.xup.msu.edu
Comp.arch.fpga Users Group	http://groups.google.com/
XESS Users Forum	www.xess.com/list_reg.html
FPGA CPU News	www.fpgacpu.org
• Xilinx Technical Answers Database (Q&A	www.xilinx.com/support/support.htm
FREE Software Tutorials	www.xilinx.com/support/techsup/tutorials/index.htm
Technical Lectures	www.xilinx.com/support/education-home.htm
Xilinx Tech Tips	www.xilinx.com/xlnx/xil_tt_home.jsp
Xilinx Tech Xclusives wv	vw.xilinx.com/support/techxclusives/techX-home.htm

Your Registration ID Number is:

9990-6970-9169